CS150 - Computer Organization and Architecture
Homework #4 - Fall 2022

Please review the “RTL Primer” document located on the course website before completing this homework.

1. Please produce the RTL for the OR instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases.

2. Please produce the RTL for the NEG instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases.