1. (25 pts) Please produce the RTL for the OR instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases. You may provide a separate sheet of paper if desired.

2. (25 pts) Please produce the RTL for the NEG instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases. You may provide a separate sheet of paper if desired.