

CS 451+551, ECE 541 Topic Schedule		
Lec	Date	Topic
1	27-Aug	Intro
2	29-Aug	Intro II
3	3-Sep	Instruction set principles
4	5-Sep	Pipelining concepts
5	10-Sep	Pipelining concepts - Performance
6	12-Sep	Pipelining concepts - Performance
7	17-Sep	Pipelining concepts - Performance
8	19-Sep	Instruction-level parallelism - concepts
9	24-Sep	Exploiting Instruction-level parallelism
10	26-Sep	ILP dynamic scheduling
11	1-Oct	Limits on Instruction-Level parallelism
12	3-Oct	Limits on Instruction-Level parallelism
13	8-Oct	Limits on Instruction-Level parallelism
14	10-Oct	Exploiting ILP with software
	15-Oct	*** Test 1 ***
15	17-Oct	Virtual memory
16	22-Oct	Virtual memory
17	24-Oct	Memory performance optimization
18	29-Oct	Memory performance optimization
19	31-Oct	Data Parallelism - SIMD
20	5-Nov	Data Parallelism - SIMD
21	7-Nov	Data Parallelism - SIMD
22	12-Nov	Data Parallelism - SIMD
23	14-Nov	Data Parallelism - SIMD
24	19-Nov	Thread Parallelism
25	21-Nov	*** Test 2 ***
26	26-Nov	Fal Break - No class
27	28-Nov	Fall Break - No class
28	3-Dec	Multicore - MIMD
29	5-Dec	Multicore - MIMD
30	9-Dec	GPGPUs
31	11-Dec	Course wrapup and review
	19-Dec	Final Exam