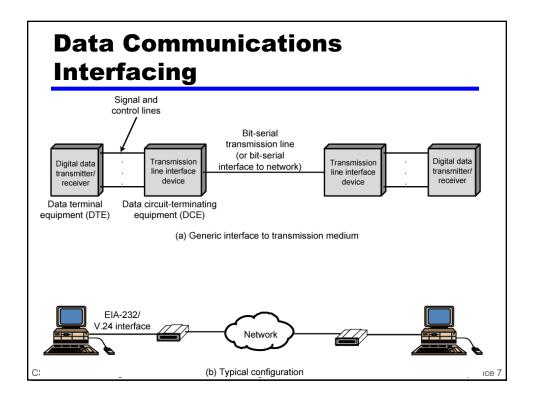
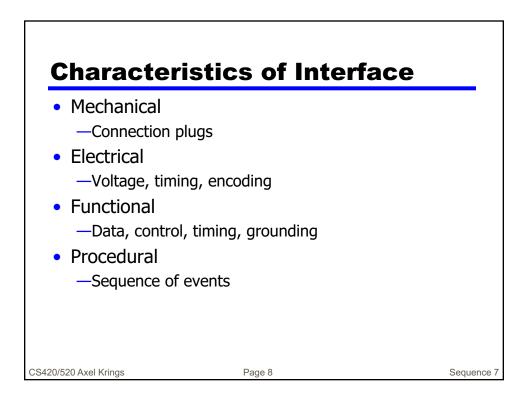
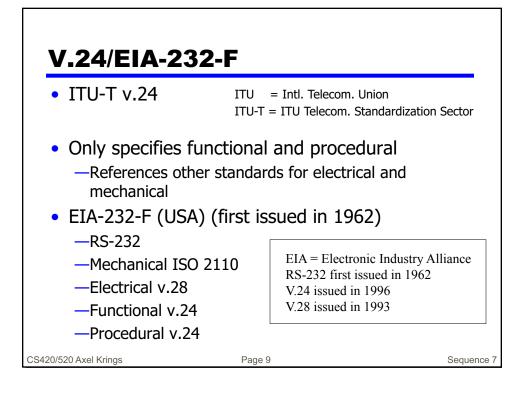
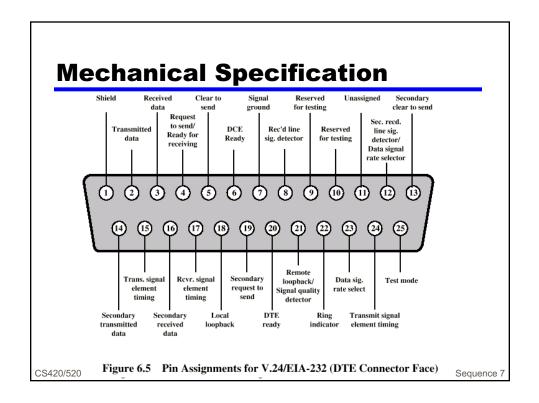


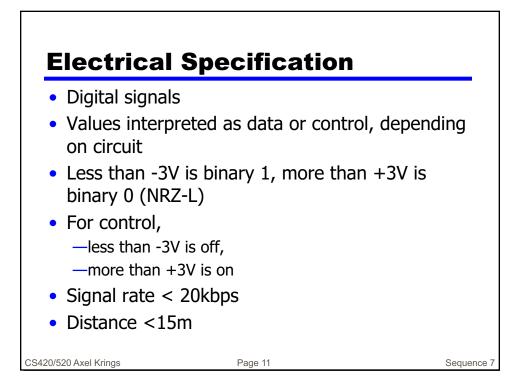
Interfacin	g	
	ing devices (or data te DTE) do not (usually) i facilities	
 Need an inter equipment (D —e.g. modem, 	,	it terminating
 DCE transmit 	s bits on medium	
 DCE commun DTE 	icates data and contro	ol info with
—Done over in	iterchange circuits	
-Clear interfac	ce standards required	
CS420/520 Axel Krings	Page 6	Sequence 7











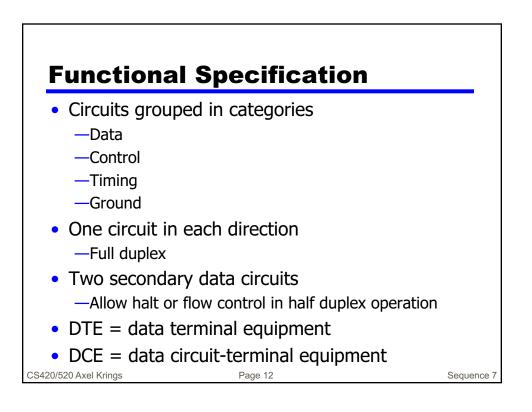
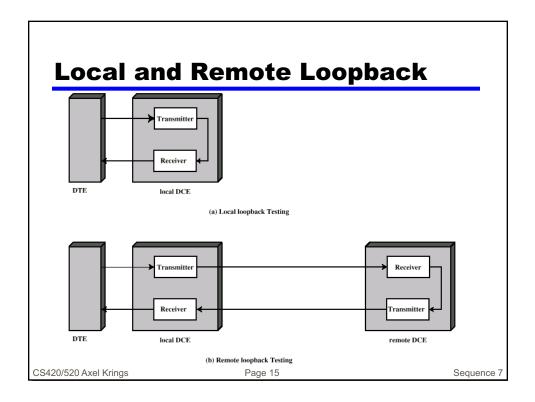
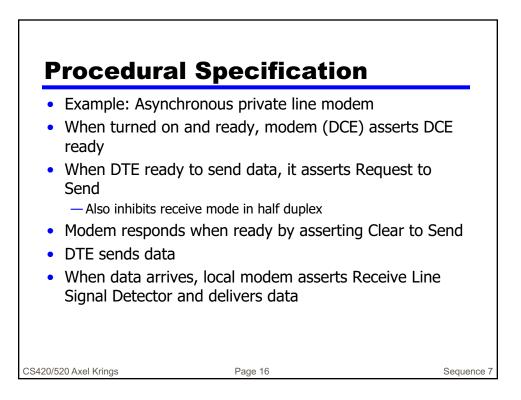


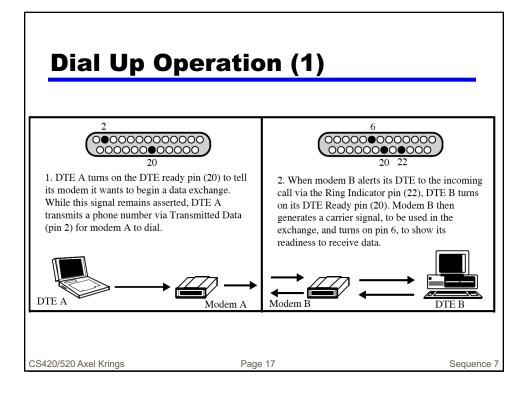
Table 6.1 V.24/EIA-232-F Interchange Circuits								
V.24	EIA- 232	Name	Direction to:	Function				
		DAT	A SIGNALS					
03	BA	Transmitted Data	DCE	Transmitted by DTE				
.04	BB	Received Data	DTE	Received by DTE				
18	SBA	Secondary Transmitted Data	DCE	Transmitted by DTE				
19	SBB	Secondary Received Data	DTE	Received by DTE				
		TIMI	NG SIGNALS					
13	DA	Transmitter signal element timing	DCE	Clocking signal; transitions to ON and OFF occur at center of each signal element				
14	DB	Transmitter signal element timing	DTE	Clocking signal; both 113 and 114 relate to signals on circuit 103				
15	DD	Receiver signal element timing	DTE	Clocking signal for circuit 104				
		G	ROUND					
02	AB	Signal ground/common return		Common ground reference for all circuits				

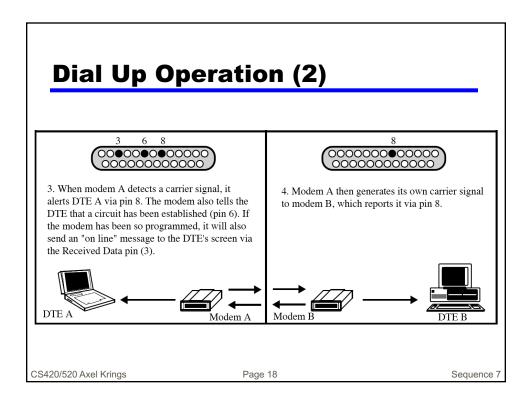
Functional Specification

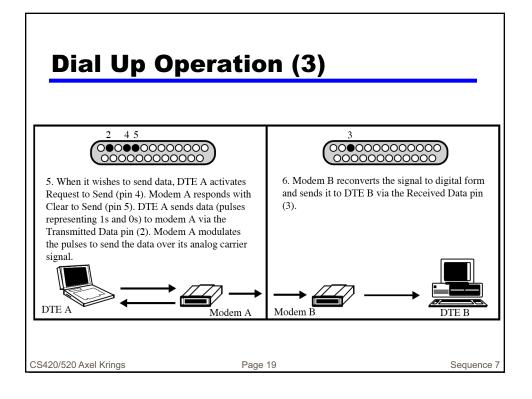
CONTROL SIGNALS								
105	CA	Request to send	DCE	DTE wishes to transmit				
106	СВ	Clear to send	DTE	DCE is ready to receive; response to Request to send				
107	CC	DCE ready	DTE	DCE is ready to operate				
108.2	CD	DTE ready	DCE	DTE is ready to operate				
125	CE	Ring indicator	DTE	DCE is receiving a ringing signal on the channel line				
109	CF	Received line signal detector	DTE	DCE is receiving a signal within appropriate limits on the channel line				
110	CG	Signal quality detector	DTE	Indicates whether there is a high probability of error in the data received				
111	CH	Data signal rate selector	DCE	Selects one of two data rates				
112	CI	Data signal rate selector	DTE	Selects one of two data rates				
133	CJ	Ready for receiving	DCE	On/off flow control				
120	SCA	Secondary request to send	DCE	DTE wishes to transmit on reverse channel				
121	SCB	Secondary clear to send	DTE	DCE is ready to receive on reverse channel				
122	SCF	Secondary received line signal detector	DTE	Same as 109, for reverse channel				
140	RL	Remote loopback	DCE	Instructs remote DCE to loop back signals				
141	LL	Local loopback	DCE	Instructs DCE to loop back signals				
142	TM	Test mode	DTE	Local DCE is in a test condition				
CS420/5	520 Axel	Krings Page	14	Sequence 7				

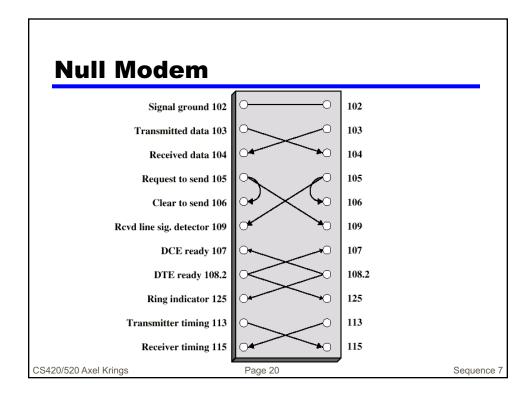


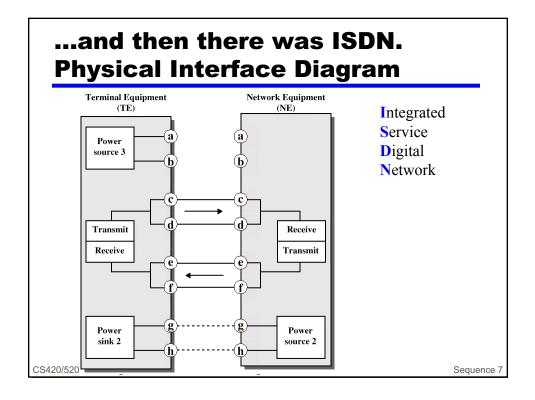


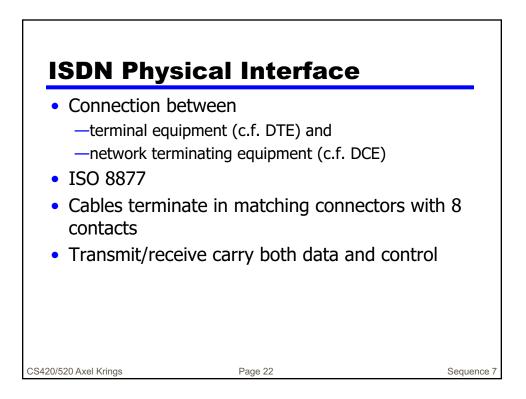


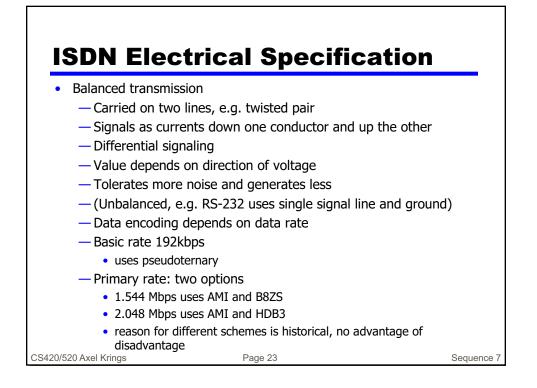


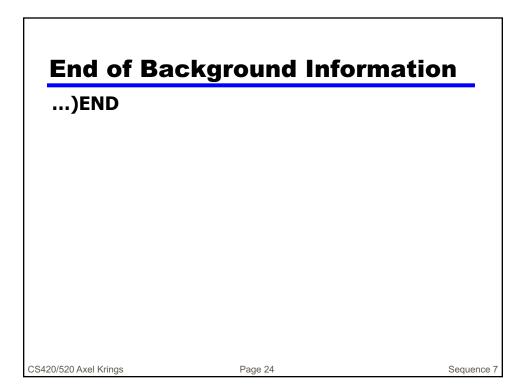


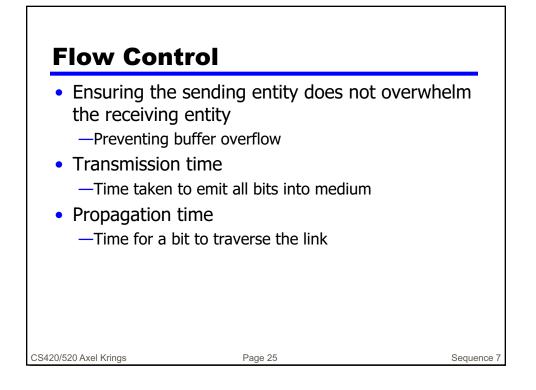


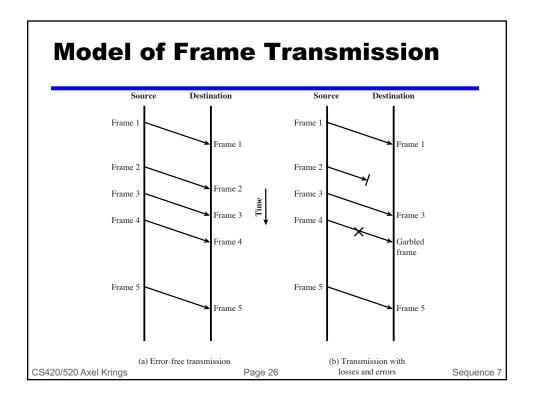


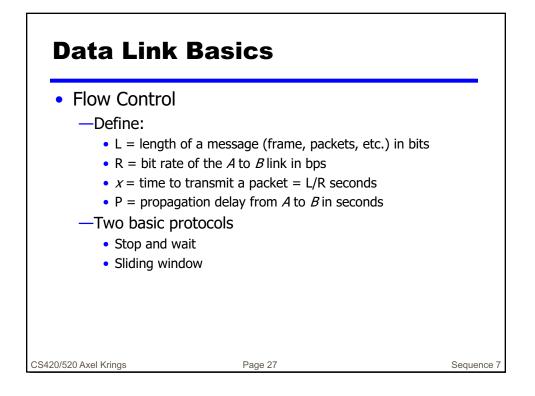


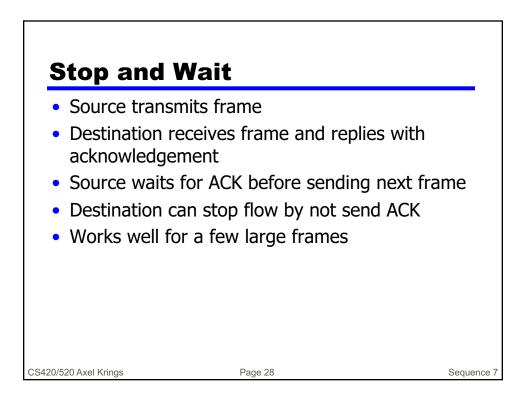


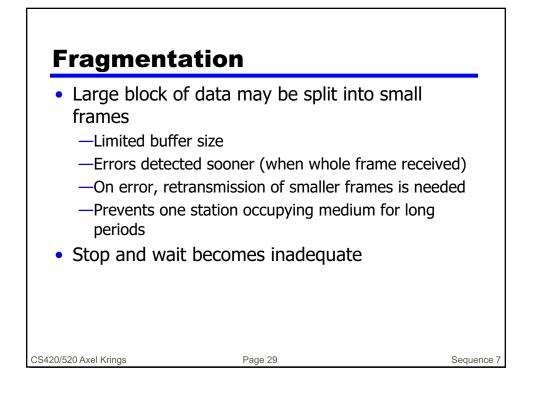


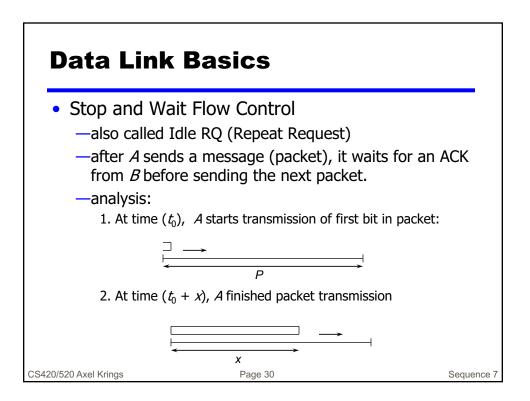


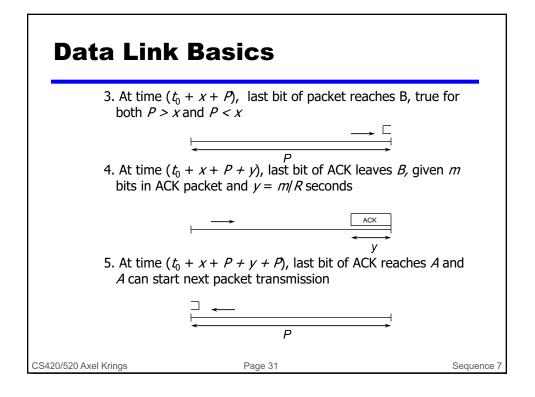


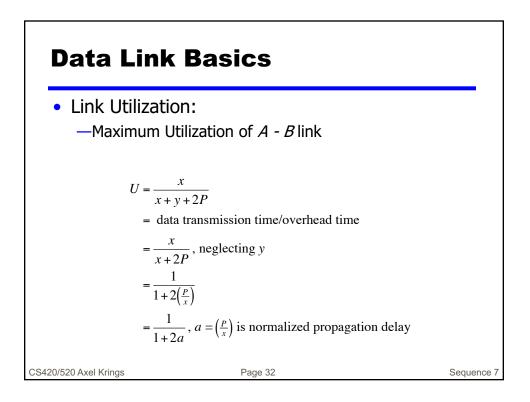


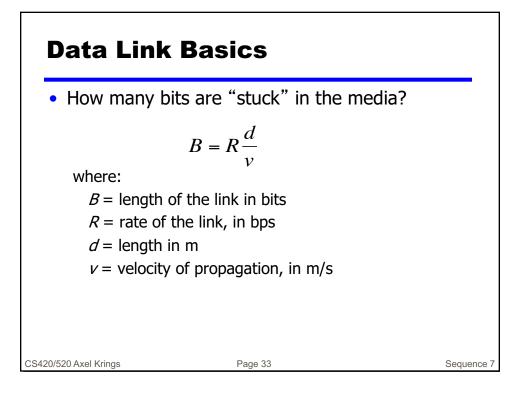


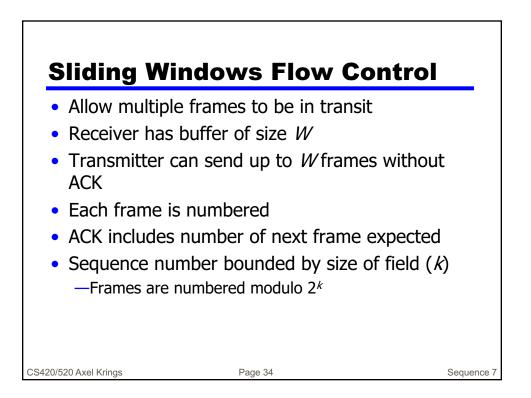


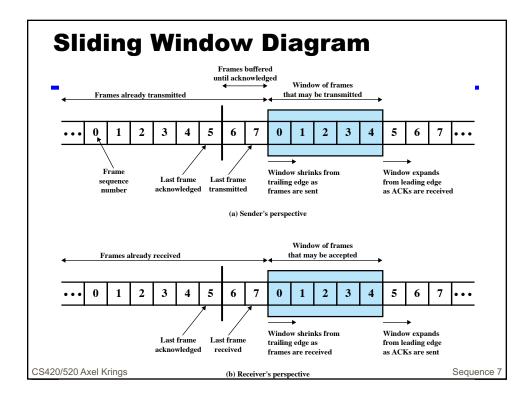


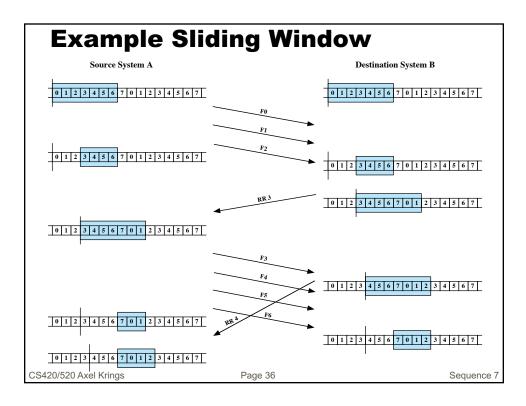


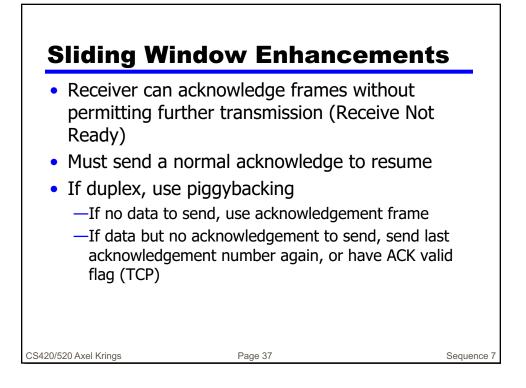


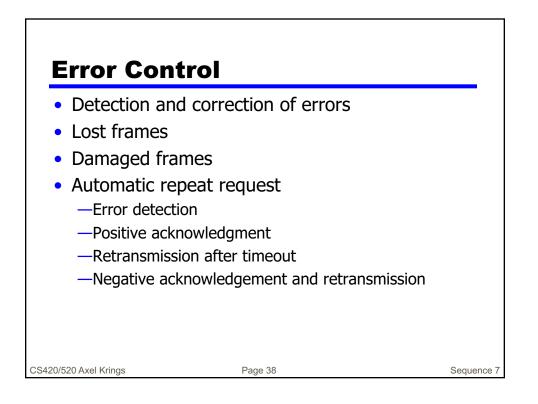


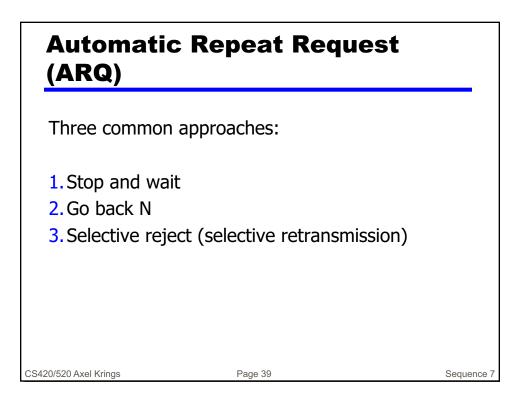


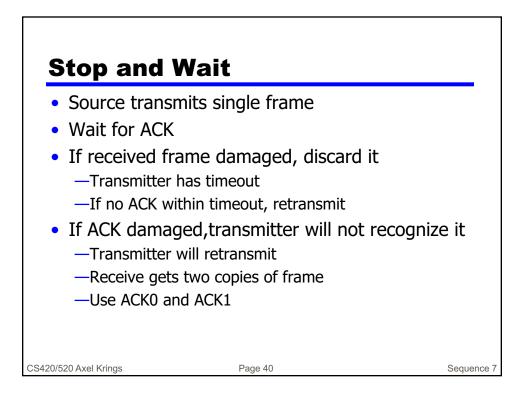


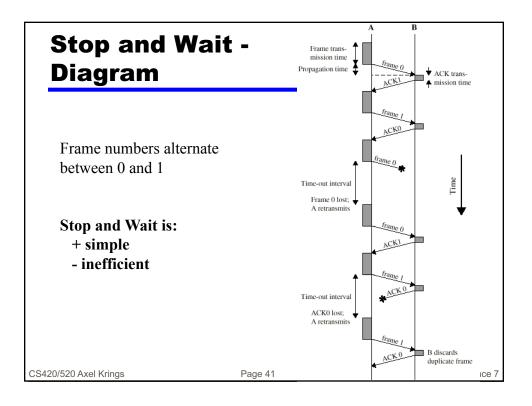


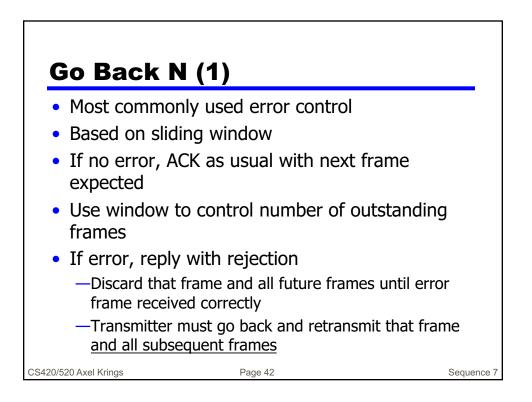


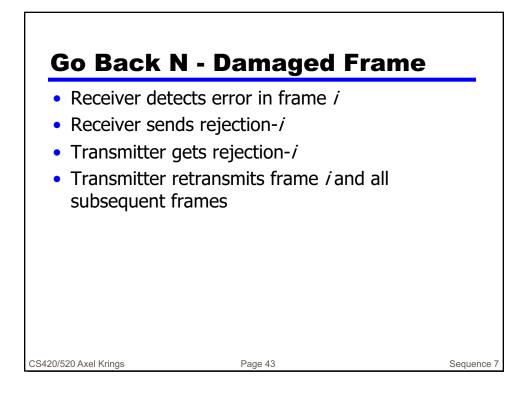


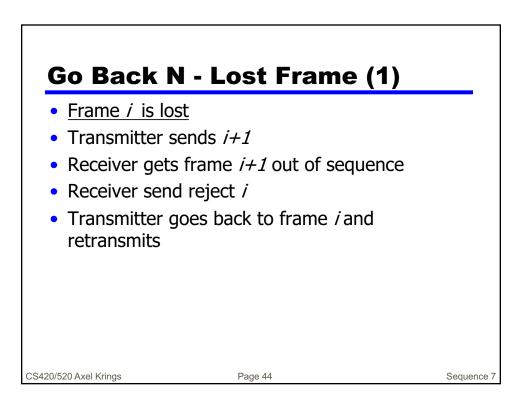


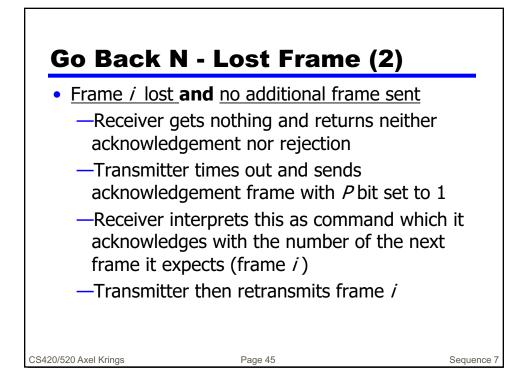


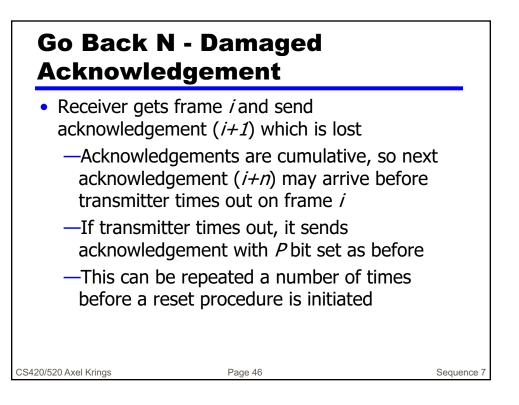


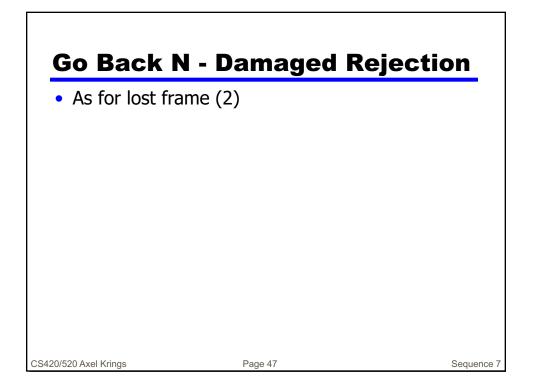


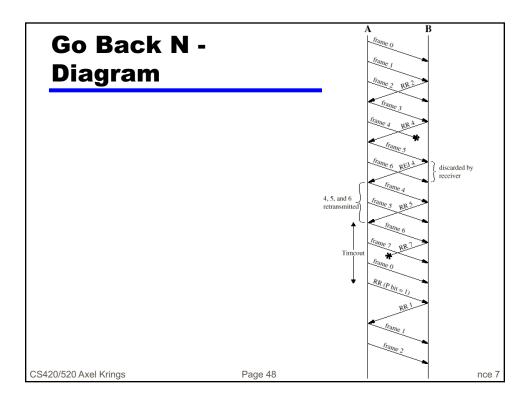


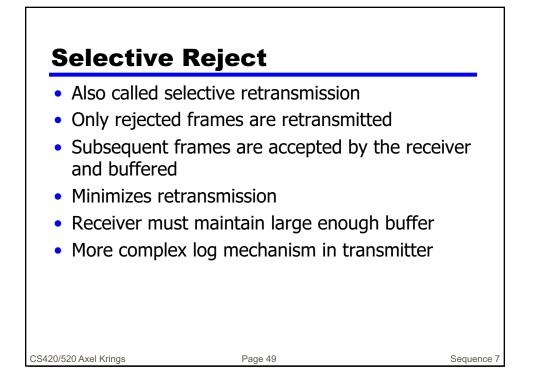


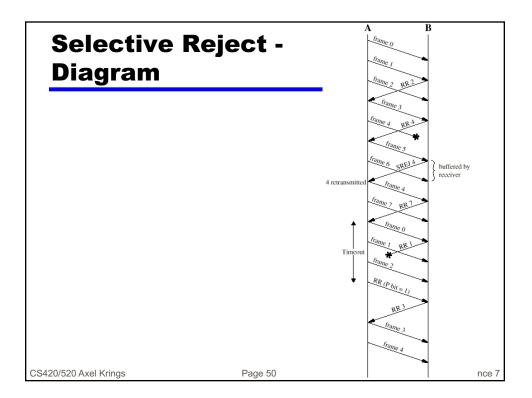


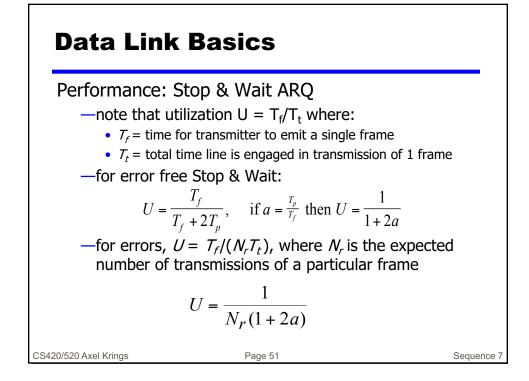


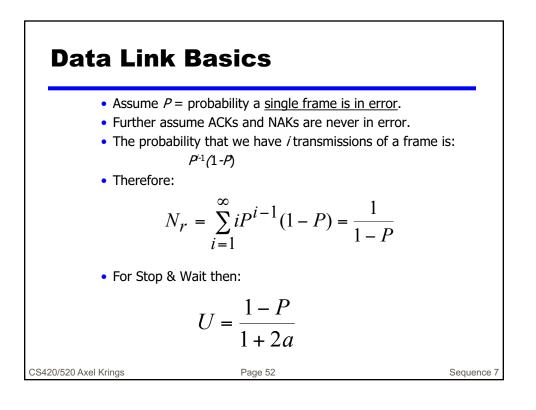


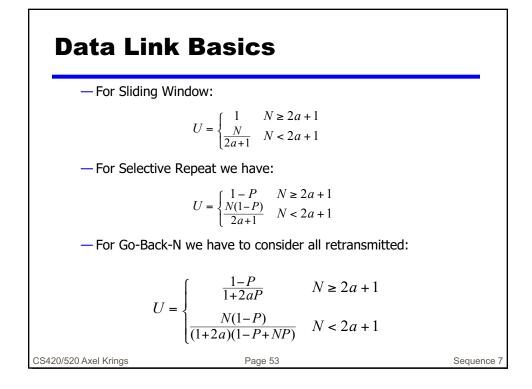


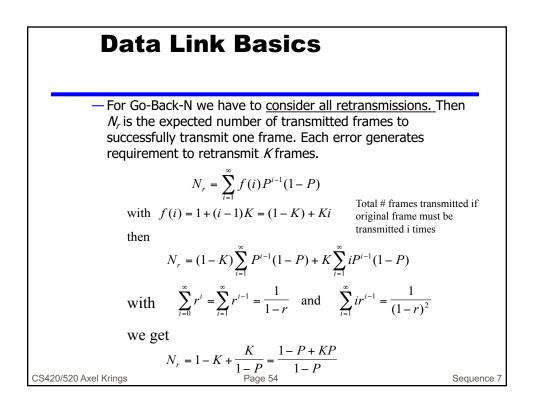


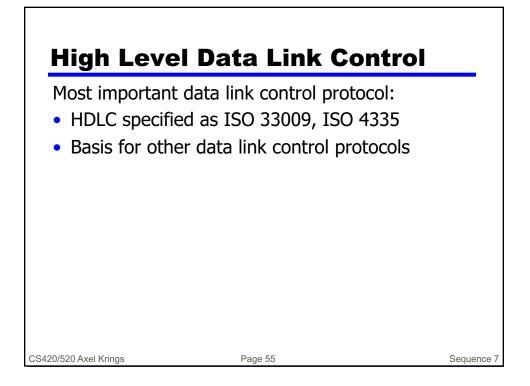


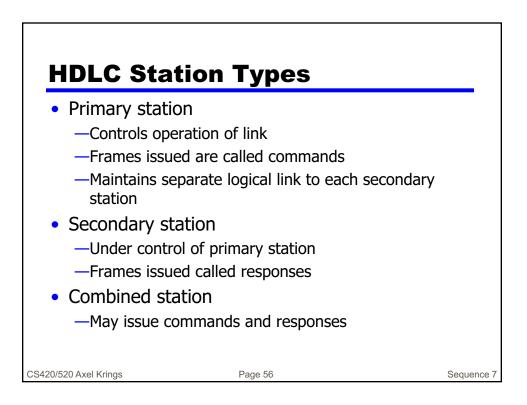


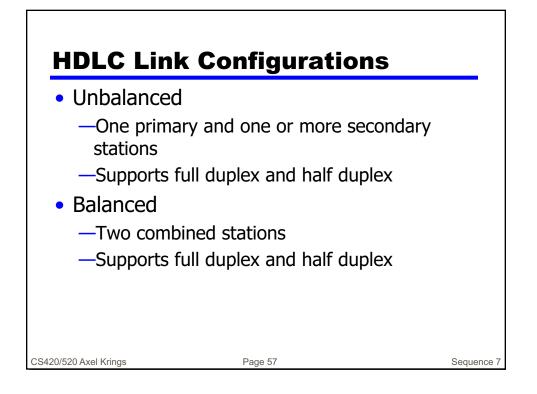


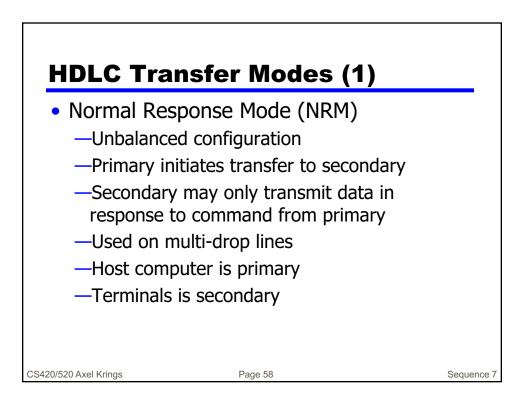


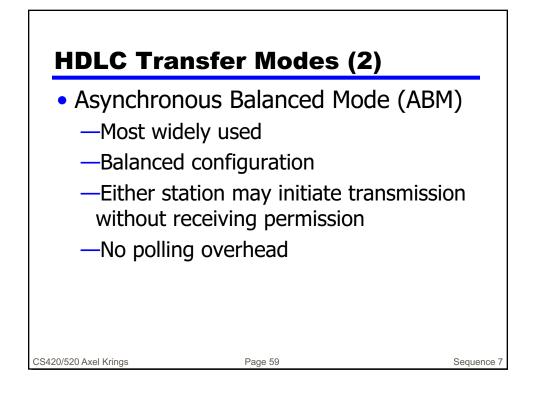


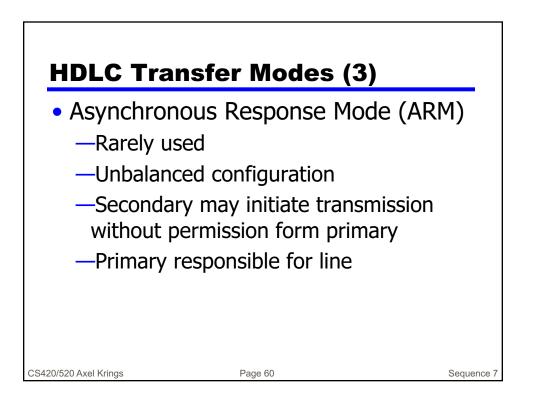


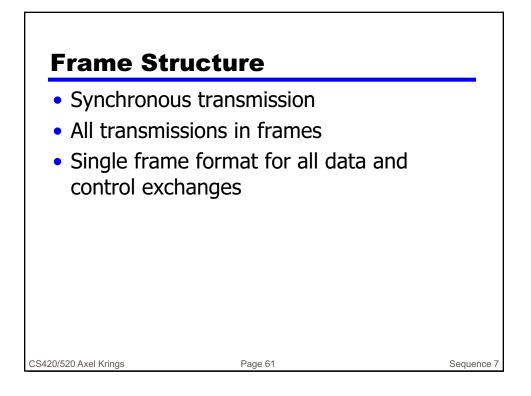


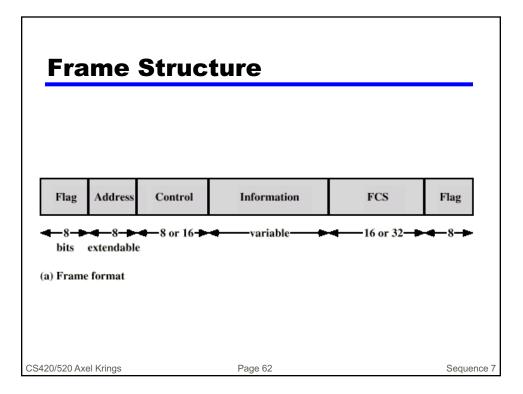


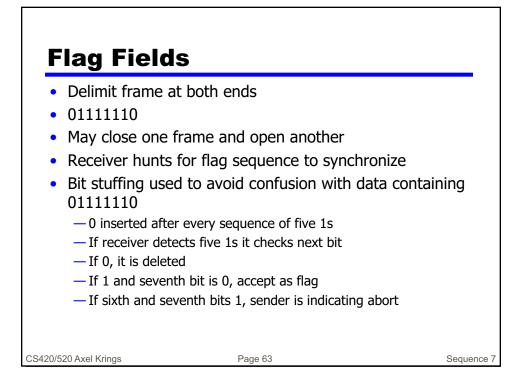


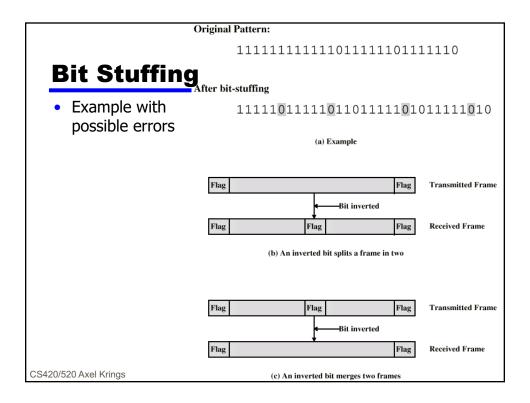


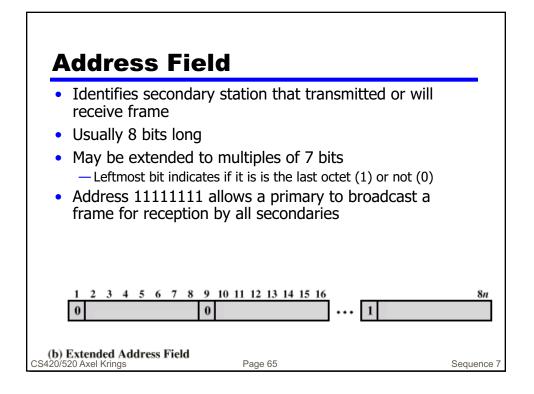


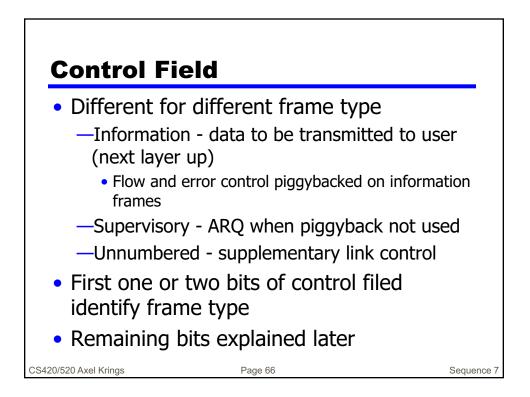




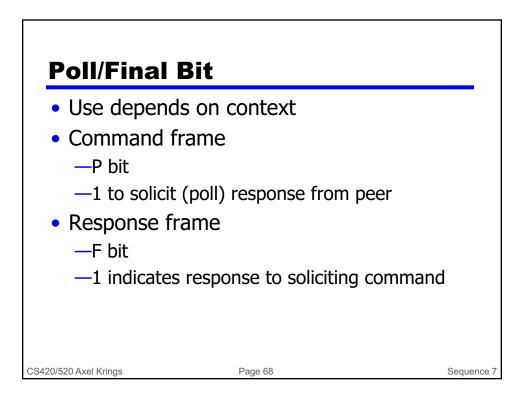


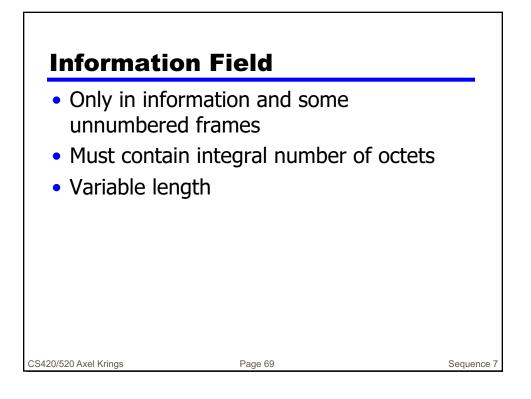


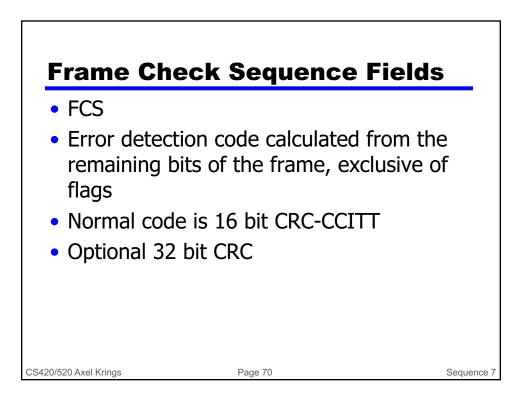


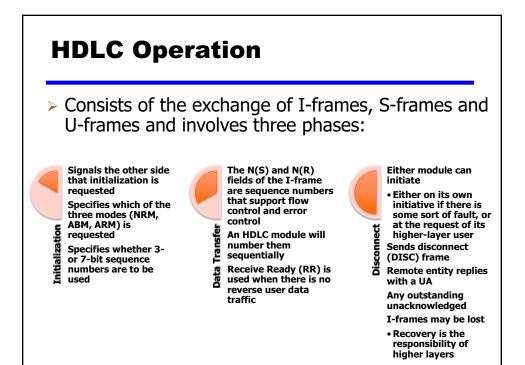


Cont	ro	b	F	ie	ld		Dia	aq	Ira	an	n							
			1	2	3	4	5	6	7	8								
I: Inform	I: Information		0 N(S)				P/F		N(R)		N/C) Cand an anna anna bar							
S: Supervisory U: Unnumbered		1	0		s	P/F		N(R)		N(S) = Send sequence number N(R) = Receive sequence number S = Supervisory function bits M = Unnumbered function bits P/F = Poll/final bit								
		d	1	1	ľ	M	P/F		Μ		F/F = Pol/final bit							
(c) 8-bit c	ontro	ol fiel	d for	mat														
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16		
Information	0				N(S)	I			P/F				N(R)]	
				~													1	
Supervisory	1	0		s	0	0	0	0	P/F				N(R)					
(d) 16-bit CS4'20/020 PAGE N		rol fie	eld fo	rma	t		гаус	5 01									Jeyden	ce 7

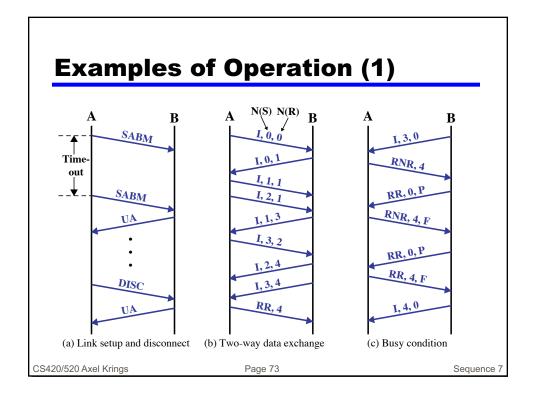


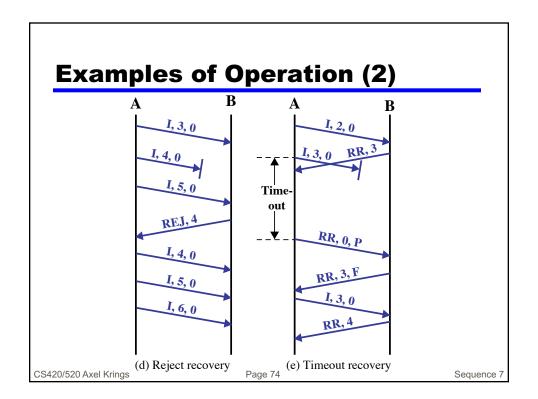






Name	Command/ Response	Description	
Information (I)	C/R	Exchange user data	
Supervisory (S)			
Receive ready (RR)	C/R	Positive acknowledgment; ready to receive I- frame	
Receive not ready (RNR)	C/R	Positive acknowledgment; not ready to receive	T -bla 7 4
Reject (REJ)	C/R	Negative acknowledgment; go back N	Table 7.1
Selective reject (SREJ)	C/R	Negative acknowledgment; selective reject	
Unnumbered (U)			
Set normal response/extended mode (SNRM/SNRME)	С	Set mode; extended = 7-bit sequence numbers	HDLC
Set asynchronous response/extended mode (SARM/SARME)	С	Set mode; extended = 7-bit sequence numbers	
Set asynchronous balanced/extended mode (SABM, SABME)	С	Set mode; extended = 7-bit sequence numbers	Command
Set initialization mode (SIM)	С	Initialize link control functions in addressed station	S
Disconnect (DISC)	С	Terminate logical link connection	and
Unnumbered Acknowledgment (UA)	R	Acknowledge acceptance of one of the set-mode commands	_
Disconnected mode (DM)	R	Responder is in disconnected mode	Response
Request disconnect (RD)	R	Request for DISC command	
Request initialization mode (RIM)	R	Initialization needed; request for SIM command	
Unnumbered information (UI)	C/R	Used to exchange control information	
Unnumbered poll (UP)	С	Used to solicit control information	
Reset (RSET)	С	Used for recovery; resets N(R), N(S)	
Exchange identification (XID)	C/R	Used to request/report status	
Test (TEST)	C/R	Exchange identical information fields for testing	(Table can be found on page
Frame reject (FRMR)	R	Report receipt of unacceptable frame	230 in the textbook)





Summary

- introduced need for data link protocols —which included background on *interfacing*
- flow control
- error control
- HDLC